

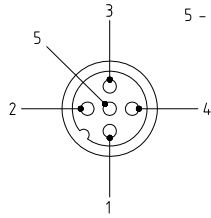
FEMALE END VIEW

J1-J4

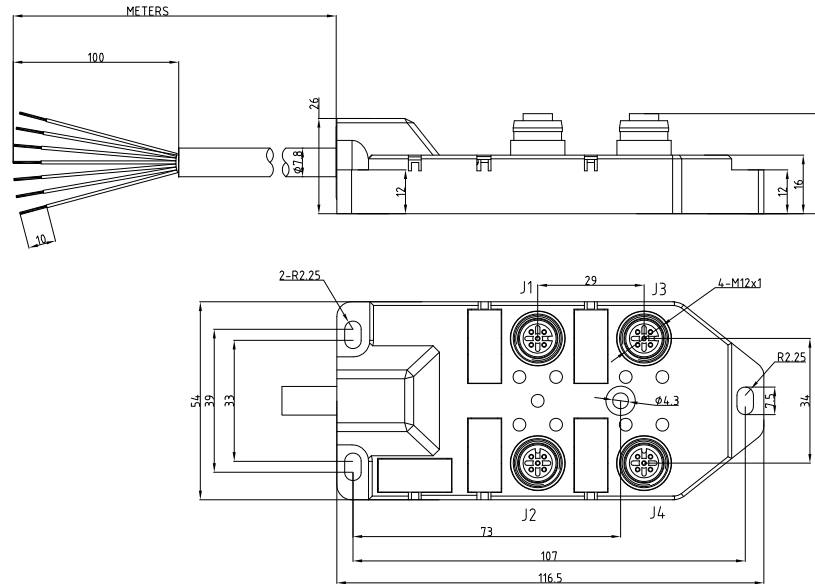
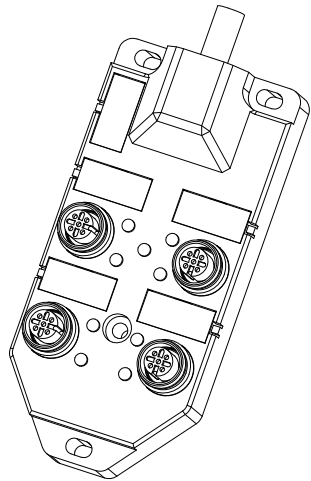
1 - V+
3 - V-
4 - Signal
5 - PE

Diagram of the Female End View of a connector. The diagram shows a circular cross-section with four pins labeled 1, 3, 4, and 5. Pin 1 is at the top, pin 3 is at the bottom, pin 4 is on the right, and pin 5 is on the left. A legend on the right identifies the pins: 1 - V+, 3 - V-, 4 - Signal, 5 - PE.

1 - V+
3 - V-
4 - Signal
5 - PE



HOUSING MATERIAL/COLOR	NYLON/BLACK
CONTACT CARRIER MATERIAL/COLOR	TPU/BLACK
CONTACTS	BRASS/GOLD
RECEPTACLE MATERIAL/PLATING	BRASS/NICKEL
O-RING MATERIAL	FPM/FKM
OPERATING CURRENT	2A PER SIGNAL, 9A TOTAL
OPERATING VOLTAGE	10-30V
OPERATING TEMPERATURE	-10°C — +70°C
PROTECTION CLASS	IEC IP67



The schematic diagram illustrates a power supply circuit with four parallel MOSFETs (J1, J2, J3, J4). Each MOSFET has a gate connected to a common gate driver circuit, a source connected to a common source terminal, and a drain connected to a common drain terminal. The gate driver circuit consists of a 10k resistor and a 0.5A current source (YE) connected to a 30V source. The input is connected to the gates, and the output is connected to the drains. The input is labeled WH, GN, YE, GY, GN/YE, BU, and BN. The output is labeled GN.

CABLE LENGTH	TOLERANCE
ALL LENGTHS	+4%(OR 50mm) OF LENGTH -0% (OR 0mm) OF LENGTH WHICHEVER IS GREATER
STRIP LENGTH	TOLERANCE
0 - 7mm	± 1.0mm
8 - 29mm	± 1.5mm
30 - 49mm	± 2.0mm
50 - 69mm	± 3.0mm
70 - 100mm	± 4.0mm
over 100mm	± 5.0mm

REV	COUNT	DESCRIPTION OF REVISIONS	BY	CHECKED	DATE

UNIT OF MEASUREMENT MILLIMETER		 Tianjin Geneuo Technology Co.,Ltd		
Approved: Date: 2018.5.16		Description: JB4M12-4N-D2		
Process: Date: 1:1				
Checked: Date:	3RD Angle Projection		www.gsee-tech.cn www.gsee-tech.com	REV A
Designed: Date:			SOURCE DRAWING-FOR REFERENCE ONLY	